



Confirmation No. 6609

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: GOPALAKRISHNAN Examiner: Unassigned  
Serial No.: 10/518,779 Group Art Unit: 2814  
Filed: June 6, 2005 Docket No.: STFD.035US  
(S02-114US)  
Title: INSULATED-GATE SEMICONDUCTOR DEVICE AND APPROACH  
INVOLVING JUNCTION-INDUCED INTERMEDIATE REGION

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 19, 2006.

By: Kelly S. Waltigney

Kelly S. Waltigney

STATUS INQUIRY

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Customer No.  
**40581**

Dear Sir:

Kindly advise the undersigned of the present status of this patent application, and please indicate when a first communication may be expected from the U.S. Patent and Trademark Office (Group 2814) regarding the examination of this application.

Respectfully submitted,

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Date: September 19, 2006

By: Robert J. Crawford

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